

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2699	(flip adj1 flop and latch and delay adj element and first with second with logic and first and second mosfets).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/01/19 08:56
L2	2675	(phase adj detector and flip adj1 flop and latch and delay adj element and first with second with logic and first and second mosfets).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/01/19 08:56
L3	1	(phase adj detector and flip adj1 flop and latch and delay adj element and first with second with logic and first and second adj mosfets).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/01/19 08:56
L4	1	(phase adj detector and flip adj1 flop and latch and delay adj element and first with second with logic and mosfets).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/01/19 08:57
L5	1	(phase adj detector and flip adj1 flop and latch and mosfets).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/01/19 08:57
L6	6	(phase adj detector and flip adj1 flop and mosfets).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/01/19 08:58
L7	7	(phase adj detector and flip adj1 flop and mosfet).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/01/19 08:58
L8	18	(phase adj detector and flip adj1 flop and mos).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/01/19 08:58